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V.J.

PATENT

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Date January 22, 2001

Ayesha S. Wilks
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas W. Voshell

Attorney Docket No.: 500080.02

Technology Center 2100

Serial No. : 09/695,756

Group Art Unit : 2133

Filed : October 24, 2000

Examiner : Not Yet Assigned

Title : METHOD AND APPARATUS FOR REDUNDANT LOCATION ADDRESSING
USING DATA COMPRESSION

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner of Patents
Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

In the Claims:

Please cancel claim 1.

Please add new claims 41-67 as follows:

52
B1

--41. A method for accessing a memory, comprising:
comparing a memory address of a memory access request to defective memory
addresses stored in a compressed format, the defective memory addresses having substitute
addresses associated and stored therewith;
where the memory address matches one of the stored defective memory
addresses, extracting the substitute address associated therewith; and

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